

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of:)
)
M. Saldana) Group: Unassigned
)
Application No: Unassigned (Parent App. No. 09/823,151)) Examiner: Unassigned
)
Filed: February 26, 2004) Docket No: LAM2P255D2
)
) Date: February 26, 2004
For: APPARATUS WITH RESOLUTION)
ENHANCEMENT FEATURE FOR IMPROVING)
ACCURACY OF CONVERSION OF REQUIRED)
CHEMICAL MECHANICAL POLISHING PRESSURE)
TO FORCE TO BE APPLIED BY POLISHING HEAD TO)
WAFER)
)

CERTIFICATE OF EXPRESS MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service on **February 26, 2004** in an envelope as "Express Mail Post Office to Addressee" service under 37 CFR §1.10, Mailing Label Number **EV381562404US**, addressed to the Commissioner for Patents, Alexandria, VA 22313-1450.

Signed: _____

Kay Marlow
Kay Marlow

INFORMATION DISCLOSURE STATEMENT

UNDER 37 CFR §§1.56 AND 1.97

Mail Stop: DD
Commissioner for Patents
Alexandria, VA 22313-1450

Dear Sir:

Enclosed is a PTO Form 1449 listing references to be made of record. Copies of each of the identified references have been previously submitted in the parent case having Serial Number 09/823,151, filed on March 29, 2001, from which priority is claimed under 35 U.S.C. § 120, and may be material to the examination of the above-identified patent application. Applicant submits this list of references in compliance with their duty of disclosure pursuant to 37 CFR §§1.56 and 1.97. The Examiner is requested to make these citations of official record in this application.

This Information Disclosure Statement is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that these references indeed constitute prior art.

It is respectfully submitted that no fee is due in conjunction with the filing of this IDS because the Information Disclosure Statement is being filed before the first Office Action. However, should the Commissioner determine that additional fees are required for filing the present Information Disclosure Statement, the Commissioner is hereby authorized to charge any such fees to our deposit account number 50-0805 (Order No: LAM2P255D2). A copy of this sheet is enclosed.

Respectfully submitted,
MARTINE & PENILLA, LLP

Chester E. Martine, Jr.
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Customer Number 25920

PATENT

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In re the application of:

M. Saldana

Application No: Unassigned (Parent App. No. 09/823,151)

Filed: February 26, 2004

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Group: Unassigned

Examiner: Unassigned

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Kay Barlow

**INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR §§1.56 AND 1.97**

Mail Stop: DD
Commissioner for Patents
Alexandria, VA 22313-1450

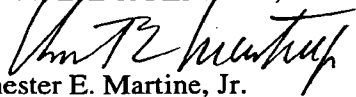
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Form 1449 (Modified) Information Disclosure Statement By Applicant (Use Several Sheets if Necessary)	Attorney Docket No:	U.S.
	LAM2P255D2	Unassigned
	Applicant:	
	M. Saldana	
	Filing Date:	Group:
	February 26, 2004	Unassigned

U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class
/CK/	A	5,623,318	04/1997	Lee	348	614
/CK/	B	5,916,012	06/1999	Pant et al.	451	41
/CK/	C	6,139,400	10/2000	Sato et al.	451	10
/CK/	D	6,140,953	10/2000	Fukuyama	341	144
/CK/	E	6,422,918	07/2002	Avanzino et al.	451	41
	F					
	G					
	H					
	I					
	J					
	K					

Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Date	Country or Patent Office	Class	Sub-class	Translation	
							Yes	No
/CK/	L	GB 2 162 977 A	12/2/86	EPO	H03M	7/00		X
	M							
	N							

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
/CK/	O	F.S. Lai, "The Architecture and Analysis of a Hybrid Number System Processor", 1992 IEEE, IBM Thomas J. Watson Research Ctr., Yorktown Heights, NY.
/CK/	P	Samueli, H., "An Improved Search Algorithm for the Design of Multiplierless FIR Filters with Powers-of-Two Coefficients", pp. 1044-1047, IEEE Transactions on Circuits and Systems, Vol. 36, No. 7, July 1989.
/CK/	Q	Avizienis, "Signed-Digit Number Representations for Fast Parallel Arithmetic", pp. 389-400, IRE Transactions on Electronic Computers, Sept. 1961.
Examiner /Charles Kasenge/		Date Considered 04/24/2008

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered.
Include copy of this form with next communication to applicant.